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Patent
Attorney's Docket No. 032001-060

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Peter Shing Fai LAM) Group Art Unit: unknown
Application No.: 09/989,529) Examiner: to be assigned
Filed: November 21, 2001)
For: MODIFICATION TO RECONFIGURABLE)
FUNCTIONAL UNIT IN A RECONFIGUR-)
ABLE CHIP TO PERFORM LINEAR)
FEEDBACK SHIFT REGISTER FUNCTION)

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**INFORMATION DISCLOSURE STATEMENT
TRANSMITTAL LETTER**

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

☒ No additional fee for submission of an IDS is required.


☐ The fee of \$180.00 (126) as set forth in 37 C.F.R. § 1.17(p) is also enclosed.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

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Alexandria, Virginia 22313-1404

By: 
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Redwood Shores, California Office
Tel: (650) 622-2333

Date: March 4, 2002



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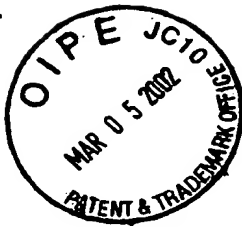
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INFORMATION DISCLOSURE STATEMENT

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Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

Non-Patent Literature Documents

Chameleon Systems, Inc., Product Overview sheet for CS2000 Reconfigurable Communications Processor, © 2000

Chameleon Systems, Inc., Family Product Brief for CS2000 Reconfigurable Communications Processor, Advance Produce Information, © 2000.

GLASKOWSKY, "Chameleon Crosses CPU, FPGA," Microdesign Resources, June 12, 2000.

Dave Bursky [Editor-in-Chief], "Comm Processor Adjusts For Task At Hand," Electronic Design, May 15, 2000 pg. 66 et seq.

Graham Mostyn, "Benefits of Reconfigurable Processors for Communication System Design," Communication Systems Design, January 2000.

"Reconfigurables hit comms processing," Electronic Engineering Times, May 22, 2000.


Crista Souza, "Semiconductors: Chameleon a quick-change artist," Electronic Buyers' News, May 22, 2000.

The documents are being submitted within 3 months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later, therefore no fee or certification is required under 37 C.F.R. § 1.97(b).

To assist the Examiner, the documents are listed on the attached form PTO-1449 and respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

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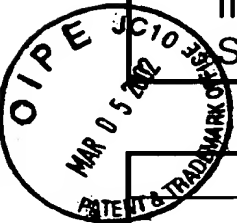
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FILING DATE
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GROUP
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Examiner Initials	Foreign Patent Document		Country	Date of Publication (MM-DD-YYYY)	Translation	
	Number	Kind Code (if known)			Yes	no

Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
	Chameleon Systems, Inc., Product Overview sheet for CS2000 Reconfigurable Communications Processor, © 2000		
	Chameleon Systems, Inc., Family Product Brief for CS2000 Reconfigurable Communications Processor, Advance Produce Information, © 2000		
	Glaskowsky, "Chameleon Crosses CPU, FPGA," Microdesign Resources, June 12, 2000.		
	Dave Bursky [Editor-in-Chief], "Comm Processor Adjusts For Task At Hand," Electronic Design, May 15, 2000 pg. 66 et seq.		
	Graham Mostyn, "Benefits of Reconfigurable Processors for Communication System Design," Communication Systems Design, January 2000		
	"Reconfigurables hit comms processing," Electronic Engineering Times, May 22, 2000		
	Crista Souza, "Semiconductors: Chameleon a quick-change artist," Electronic Buyers' News, May 22, 2000		
Examiner Signature		Date Considered	

(05/01)